

## Lab #11 – Design of a Ring Counter

**Objective:** In this lab you are to design a ring counter.

1. Using [74LS74](#) flip flops and other gates, design a mod 6 ring counter.
2. Implement your circuit first in Electronic Workbench and verify its operation. Once satisfied, construct it on your breadboard. Upon completion, have TA or instructor verify the design and sign both the lab cover page and your notebook.
3. Implement your design using Verilog.

In preparation for this lab, each student should draw a complete logic schematic for the circuit he/she intends to construct.