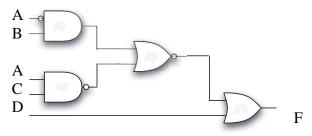
First Name: ____ Last Name: ___

For full credit, you need to show your work neatly and box your answers.

- 1. Using AND and OR gates, draw the logic diagrams for the following Boolean expressions without expanding or simplifying them.
 - a. Y = (A'+B')C + B(A+C)
 - b. W = (A+B')(C+D')
- 2. Write the Boolean expression equivalent to the following logic circuit. Do not simplify!



3. Write a truth table for

$$F(A, B, C) = (\overline{A + B})(B + \overline{C})$$

- 4. Find the dual of
 - a. F = A'B + B'C' + D'
 - b. $F(A, B, C) = (\overline{A + B})(B + \overline{C})$
- 5. Find the complement of
 - a. F = A'B + B'C' + D'
 - b. $F(A, B, C) = (\overline{A+B})(B+\overline{C})$
- 6. Demonstrate by means of truth tables the validity of the following identities
 - A. DeMorgan's law for three variables: (X+Y+Z)' = X'Y'Z' and

$$(XYZ)' = X' + Y' + Z'$$

- B. (X+Y) X = X
- 7. Simplify the following Boolean expression as much as possible.
 - a. ABC + A'B + ABC'
 - b. (X+Y)'(X'+Y')
 - c. (BC' + A'D)(AB' + CD')
 - d. X'YZ + XZ
 - e. XY + X(WZ + WZ')
- 8. Reduce the following Boolean expression to the indicated number of literals:
 - a. A'C' + ABC + AC'

to three literals

b. (A' + C)(A' + C')(A + B + C'D)

to four literals

c. A'B(D'+C'D) + B(A+A'CD)

to one literal

Due Date: Thursday 9/20/2018