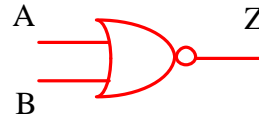


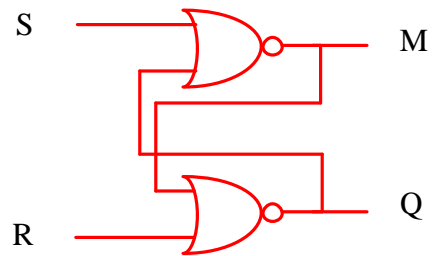
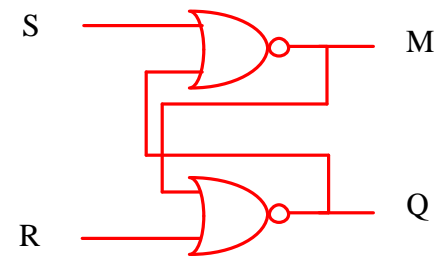
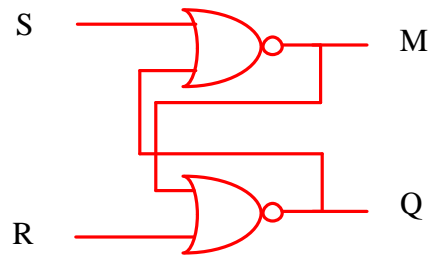
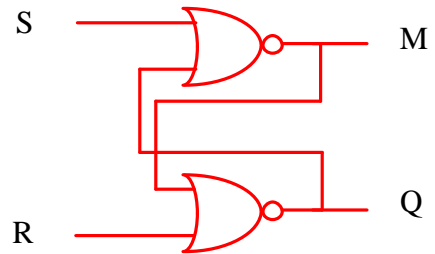
NOR gate property:

| A | B | Z |
|---|---|---|
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 0 |



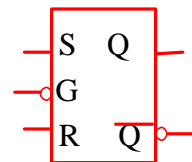
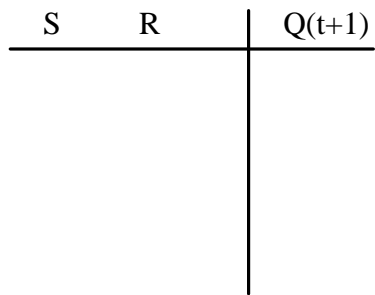
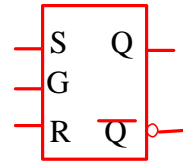
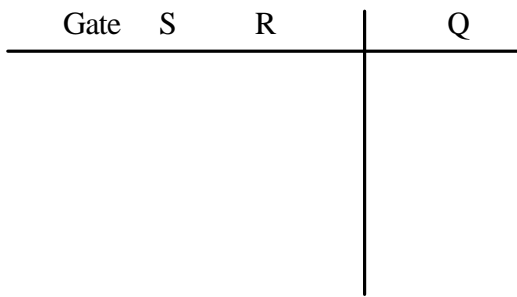
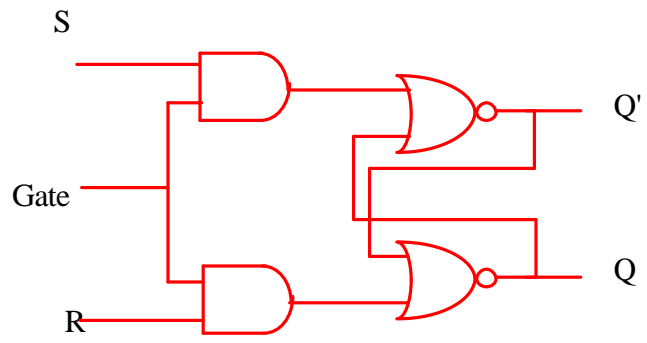
Cross coupled NOR gates:

| S | R | Q | M |
|---|---|---|---|
| 0 | 0 | | |
| 0 | 1 | | |
| 1 | 0 | | |
| 1 | 1 | | |



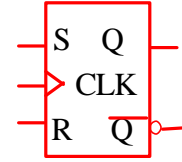
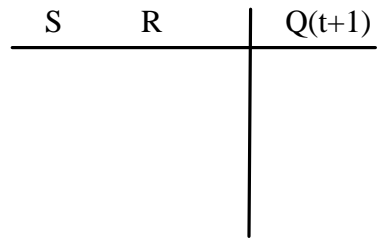
| S | R | Q |
|---|---|---|
| 0 | 0 | |
| 0 | 1 | |
| 1 | 0 | |
| 1 | 1 | |

A circuit diagram of a cross-coupled NOR gate latch. It has two input lines labeled 'S' and 'R' on the left. The output line is labeled 'Q' on the right. The circuit consists of two NOR gates. The output of the top NOR gate is connected to the input of the bottom NOR gate, and the output of the bottom NOR gate is connected to the input of the top NOR gate.

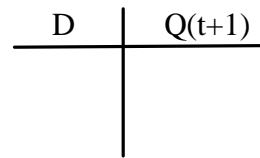
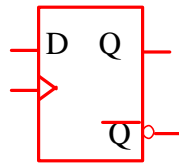
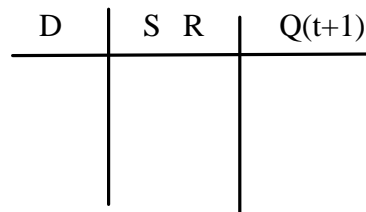
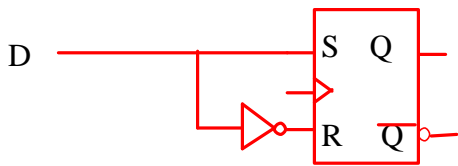


Flip flops:

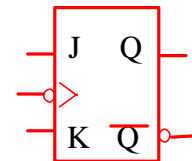
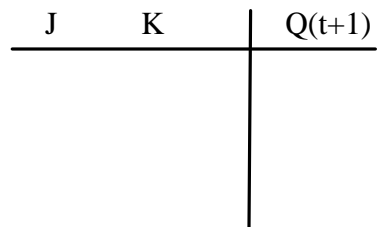
1. S-R flip-flop



2. D flip-flop



3. J K flip-flop



4. T flip-flop

